

ETC - VI

GOA UNIVERSITY

ENGINEERING EXAMINATION SYSTEM
COLLEGEWISE STUDENT RESULT REGISTER FOR VI TH SEMESTER EXAM

PAGE : 8

[04 ELECTRONICS & TELECOMM.]
COLLEGE : GOA COLLEGE OF ENGINEERING

** RC 2007-08 **
EXAM YEAR : NOV-2016

SEAT NO.	P.R. NO.	ARE CD.	PRO VI.	SEX	NAME OF THE CANDIDATE	THEORY			SEM. 6	SEM. 5	GRAND	REMARKS
						MAX. 100	SESS 25	TOTAL 125	TOTAL MAX. 850 MIN. 340 PRACTICAL 50 OR 25	TOTAL MAX. 850 MIN. 340 ORAL 50 OR 25	TOTAL MAX. 1700 MIN. 0680	
0033	201204014	(E)	N	M	ASHIS SHARMA							** RESULT WITHHELD - (SEM. 5 NOT PASSED) **
					COMMUNICATION ENGINEERING II	041	011	052+		023+		
					PERIPHERAL DEVICES & INTERFACING	046	005	051P				
					POWER ELECTRONICS	059	011	070+				
					ANTENA & WAVE PROPAGATION	059	015	074+				
					ELECTRONIC INSTRUMENTATION	040	012	052+				
					VLSI TECHNOLOGY & DESIGN	042	013	055+		026+		
0034	201305587	(E)	N	M	DESSAI MANDAR VIKAS							
					COMMUNICATION ENGINEERING II	059	017	076+		040+		
					PERIPHERAL DEVICES & INTERFACING	056	007	063P				
					POWER ELECTRONICS	078	017	095+				
					ANTENA & WAVE PROPAGATION	069	023	092+				
					ELECTRONIC INSTRUMENTATION	053	016	069+				
					VLSI TECHNOLOGY & DESIGN	070	016	086+		034+		
									555	P 401	\$03 P	0956 \$03 C
0035	201104825	(E)	N	M	JOSHI ADITYA KALIDAS							** RESULT WITHHELD - (SEM. 5 NOT PASSED) **
					COMMUNICATION ENGINEERING II	000	006	006A		000A		
					PERIPHERAL DEVICES & INTERFACING	000	000	000A				
					POWER ELECTRONICS	062	017	079+				
					ANTENA & WAVE PROPAGATION	035	\$5 014	049+	\$5			
					ELECTRONIC INSTRUMENTATION	067	011	078+				
					VLSI TECHNOLOGY & DESIGN	008	015	023F		038E		
0036	200907860	(E)	N	F	KAMBLI KENAUDEKAR KUMARSEN TWINKLE							** RESULT WITHHELD - (SEM. 5 NOT PASSED) **
					COMMUNICATION ENGINEERING II	042	000	042F		020+		
					PERIPHERAL DEVICES & INTERFACING	000	001	001F				
					POWER ELECTRONICS	050	013	063+				
					ANTENA & WAVE PROPAGATION	045	010	055+				
					ELECTRONIC INSTRUMENTATION	035	\$5 010	045+	\$5			
					VLSI TECHNOLOGY & DESIGN	049	010	059+		032+		
0037	201104817	(E)	N	M	KANEKAR SOHAN MADHUSUDAN							** RESULT WITHHELD - (SEM. 5 NOT PASSED) **
					COMMUNICATION ENGINEERING II	046	012	058+		020+		
					PERIPHERAL DEVICES & INTERFACING	040	006	046+	\$4			
					POWER ELECTRONICS	045	006	051+				
					ANTENA & WAVE PROPAGATION	043	013	056+				
					ELECTRONIC INSTRUMENTATION	009	013	022F				
					VLSI TECHNOLOGY & DESIGN	010	011	021F		028+		

P: PASS CLASS; F: FAILS; A: ABSENT; N: NON APPEARANCE D: FIRST CLASS WITH DISTINCTION; I: FIRST CLASS; S: SECOND CLASS;
C: PASSED IN COMPARTMENT; X: NOT ANSWERED SEM. 5

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						MAX. 100	SESS 25	TOTAL 125	MAX. 850 MIN. 340 PRACTICAL 50 OR 25	TOTAL 850 MIN. 340 DRAL 50 OR 25	MAX. 1700 MIN. 0680				
0038	201305734	(E)	N	M	PADGAONKAR RONIT VIRENDRA	038	\$2 018	056+	\$2		037+				
					COMMUNICATION ENGINEERING II	046	008	054P							
					PERIPHERAL DEVICES & INTERFACING	051	014	065+							
					POWER ELECTRONICS	055	015	070+							
					ANTENA & WAVE PROPAGATION	042	017	059+							
					ELECTRONIC INSTRUMENTATION	048	017	065+							
					VLSI TECHNOLOGY & DESIGN						032+				
									438	\$02 P	391	\$10 C	0829	\$12 C	
0039	201008305	(E)	N	M	REVONKAR LOHITH S	000	007	007A			030+				
					COMMUNICATION ENGINEERING II	000	000	000A							
					PERIPHERAL DEVICES & INTERFACING	040	014	054+							
					POWER ELECTRONICS	000	018	018A							
					ANTENA & WAVE PROPAGATION	050	011	061E							
					ELECTRONIC INSTRUMENTATION	000	010	010A							
					VLSI TECHNOLOGY & DESIGN						034+				
									214	F	435	\$10 P	0649	\$10 F	
0040	201006494	(E)	N	M	SAVANT VINAY	035	003	038F			032+				
					COMMUNICATION ENGINEERING II	030	005	035F							
					PERIPHERAL DEVICES & INTERFACING	050	011	061+							
					POWER ELECTRONICS	045	011	056+							
					ANTENA & WAVE PROPAGATION	036	\$4 009	045+	\$5						
					ELECTRONIC INSTRUMENTATION	056	006	062+							
					VLSI TECHNOLOGY & DESIGN						030+				
									359	\$05 F	408	\$15 C	0767	\$20 F	
0041	201305733	(E)	N	M	SHAIKH ZEID	057	010	067+			022+				
					COMMUNICATION ENGINEERING II	063	011	074P							
					PERIPHERAL DEVICES & INTERFACING	065	013	078+							
					POWER ELECTRONICS	058	016	074+							
					ANTENA & WAVE PROPAGATION	047	017	064+							
					ELECTRONIC INSTRUMENTATION	050	011	061+							
					VLSI TECHNOLOGY & DESIGN						038+				
									478	P	500	C	0978	C	
0042	201203914	(E)	N	M	SHIRODKAR NIKHIL PRATAP	042	004	046+	\$4		039+				
					COMMUNICATION ENGINEERING II	000	013	013A							
					PERIPHERAL DEVICES & INTERFACING	052	013	065+							
					POWER ELECTRONICS	044	005	049+	\$1						
					ANTENA & WAVE PROPAGATION	040	008	048+	\$2						
					ELECTRONIC INSTRUMENTATION	037	\$3 010	047+	\$3						
					VLSI TECHNOLOGY & DESIGN						034+				

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					MAX. 100	SESS 25	TOTAL 125	TOTAL MAX. 850 MIN. 340	PRACTICAL 50 OR 25	TOTAL MAX. 850 MIN. 340	DRAL 50 OR 25	MAX. 1700 MIN. 0680		
0043	201306044	(E)	N	F	YUNITA PANDEY									
					COMMUNICATION ENGINEERING II	047	014	061+			036+			
					PERIPHERAL DEVICES & INTERFACING	042	008	050P						
					POWER ELECTRONICS	066	015	081+						
					ANTENA & WAVE PROPAGATION	069	018	087+						
					ELECTRONIC INSTRUMENTATION	035	\$5 014	049+	\$5					
					VLSI TECHNOLOGY & DESIGN	040	016	056+						
									452	032+	\$05 P 405	P	0857	\$05 C

READ BY *[Signature]*

CHECKED BY *[Signature]*

23/02/2017

DECLARED ON

[Signature]
23/2/2017
ASST. REGISTRAR-E (PROF)

[Signature]
23/2/17
CONTROLLER OF EXAMINATIONS

[Signature]
YVReddy
REGISTRAR